New RADX PXIe-GPUs Deliver Up To 30.3 FP32 TFLOPS for T&M Apps

Press / Sales Contact:
Ross Q. Smith
info@radxtech.com
+1 (619) 677-1849 x1

RADX TECHNOLOGIES, INC.
Knowledge is Good!
www.radxtech.com

New RADX Catalyst PXIe-GPUs Include NVIDIA Ada L4 Data Center, RTX 4000 SFF and RTX 2000 GPUs for Compute-Intensive Signal, Image & Video Processing, ML / DL Training & Inference and LLM Applications
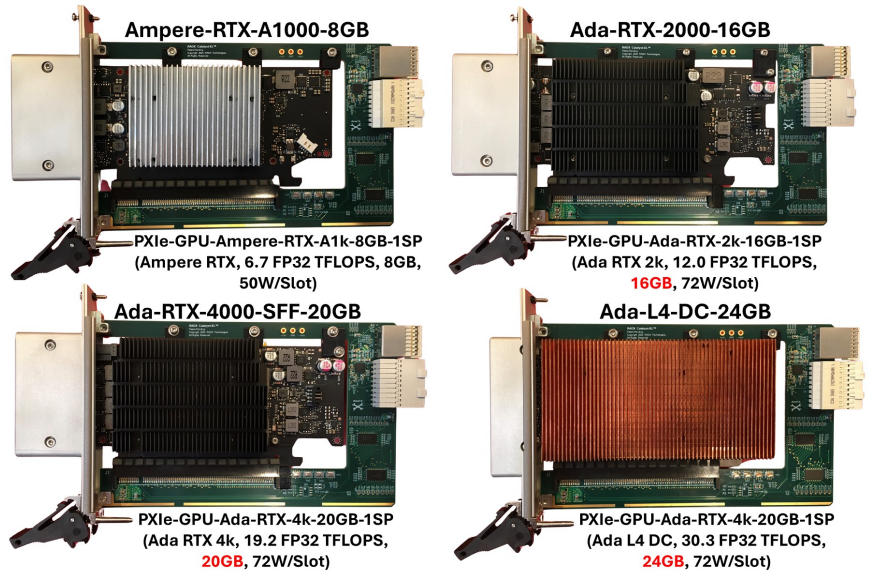
File: New RADX In-Chassis PXIe-GPUs Deliver Up To 30.3 FP32 TFLOPS Announced at NI Connect 2025 28APR2025 V1.8.doc

Fort Worth, Texas, USA, Monday, 28APR2026, RADX® Technologies, Inc. ("RADX"), today at NI Connect 2025, announced four new Catalyst-X "In-Chassis" PXIe-GPU™ Modules that combine RADX' Patent-Pending Catalyst-X PXIe Carrier Technology with powerful, easy-to-program NVIDIA Ampere and Ada Professional GPUs. The new models expand RADX in-chassis PXIe-GPU offerings with units that support up to 24GB of GDDR6 and 30.3 FP32 TFLOPS, providing unmatched compute performance and value for compute-intensive PXIe Test & Measurement (T&M) applications in Automotive AD/ADAS, Electronic Warfare (EW) and other segments.
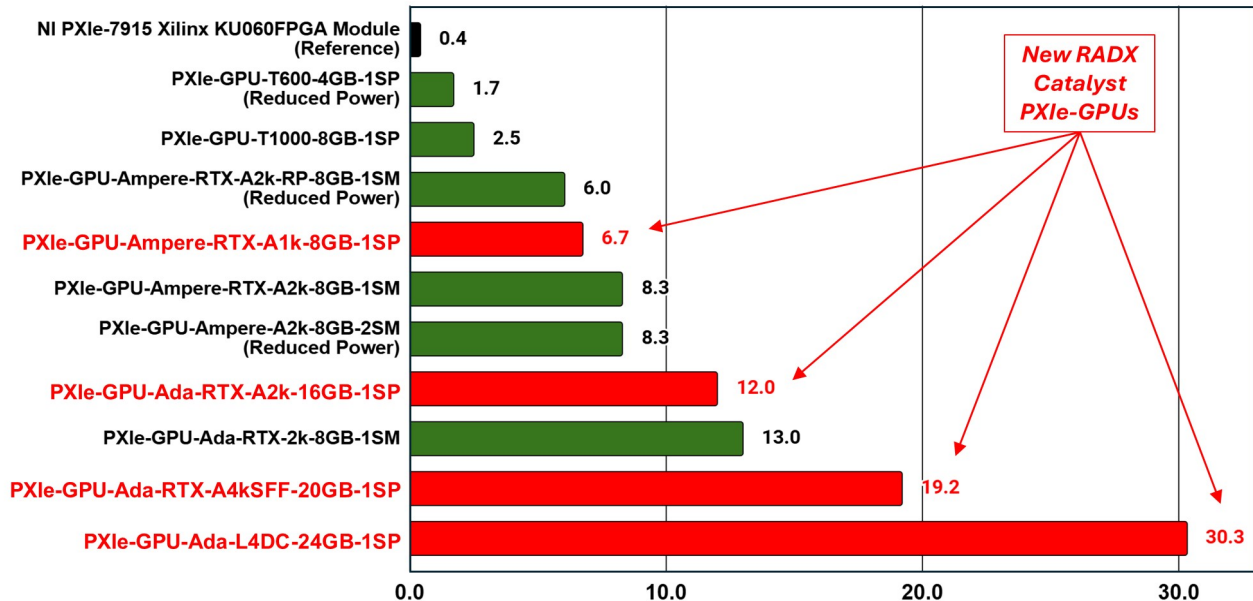
Unprecedented PXIe Performance

The new models include a low-cost module based on the Ampere RTX A1000 GPU and three advanced modules based on Ada RTX 2000, RTX 4000 SFF and L4 Data Center GPUs with 16, 20 and 24 GB of GDDR6 and 12.0, 19.2 and 30.3 FP32 TFLOPS, respectively. These new GPUs represent the highest compute performance PXIe modules available today and are ideal for cost-sensitive, yet demanding signal/image/video processing and real-time Machine Learning / Deep Learning (ML / DL) inference and training applications, including Retrieval-Augmented Generation Large Language Models (RAG LLMs)– all which require extreme memory and compute capabilities.

RADX Catalyst-X PXIe-GPUs based on NVIDIA Ada Professional GPUs Deliver up to 30.3 FP32 TFLOPS for T&M Apps

Ampere-RTX-A1000-8GB
PXIe-GPU-Ampere-RTX-A1k-8GB-1SP
(Ampere RTX, 6.7 FP32 TFLOPS, 8GB, 50W/Slot)

Ada-RTX-2000-16GB
PXIe-GPU-Ada-RTX-2k-16GB-1SP
(Ada RTX 2k, 12.0 FP32 TFLOPS, 16GB, 72W/Slot)

Ada-RTX-4000-SFF-20GB
PXIe-GPU-Ada-RTX-4k-20GB-1SP
(Ada RTX 4k, 19.2 FP32 TFLOPS, 20GB, 72W/Slot)

Ada-L4-DC-24GB
PXIe-GPU-Ada-RTX-4k-20GB-1SP
(Ada L4 DC, 30.3 FP32 TFLOPS, 24GB, 72W/Slot)

NVIDIA RTX | **BAA / TAA Compliant** | **USA** *RADX Catalyst-X PXIe-GPUs and Orca-C2X PXIe-eGPUs are Designed & Assembled in the USA*

RADX In-Chassis PXIe-GPU Performance (FP32 TFLOPS, 11APR2025, Source: NVIDIA)

Module	FP32 TFLOPS
NI PXIe-7915 Xilinx KU060FPGA Module (Reference)	0.4
PXIe-GPU-T600-4GB-1SP (Reduced Power)	1.7
PXIe-GPU-T1000-8GB-1SP	2.5
PXIe-GPU-Ampere-RTX-A2k-RP-8GB-1SM (Reduced Power)	6.0
PXIe-GPU-Ampere-RTX-A1k-8GB-1SP	6.7
PXIe-GPU-Ampere-RTX-A2k-8GB-1SM	8.3
PXIe-GPU-Ampere-A2k-8GB-2SM (Reduced Power)	8.3
PXIe-GPU-Ada-RTX-A2k-16GB-1SP	12.0
PXIe-GPU-Ada-RTX-2k-8GB-1SM	13.0
PXIe-GPU-Ada-RTX-A4kSFF-20GB-1SP	19.2
PXIe-GPU-Ada-L4DC-24GB-1SP	30.3

New RADX Catalyst PXIe-GPUs

"With up to 30.3 FP32 TFLOPS, 24GB of GDDR6 and PCIe Gen 4 x8 support, these new Catalyst PXIe-GPUs are the most powerful compute accelerators available in PXIe today," said Ross Q. Smith, RADX CEO. "Now, for the first time ever, test system integrators and end-users can use popular and easy GPU software programming techniques to develop and deploy a wide variety of low-latency, wideband, signal, image and video processing applications, combined with ML/DL training and inference applications including 70 billion parameter RAG LLMs that use two of our Ada L4 DC PXIe-GPUs in parallel."

Ease-of-Programming via LabVIEW, Python, C/C++ and MATLAB

With their intrinsic ease-of-programming, NVIDIA GPUs have rapidly become the most popular compute accelerators in the world today, with literally millions of engineers, developers, computer scientists and others using NVIDIA GPUs to accelerate their applications. Catalyst PXIe-GPUs are essentially NVIDIA Pro GPUs repackaged for PXIe. So, they also feature outstanding ease-of-programming by directly supporting MATLAB™, LabVIEW™, Python and C/C++, accelerated via NVIDIA CUDA®, OpenCL® and TensorFlow®. These new PXIe-GPU models support popular computing frameworks including PyTorch™, Ansys™, FFmpeg, and others, which dramatically reduce development time and costs for Windows and Linux operating environments. For LabVIEW users, RADX PXIe-GPUs are supported by NGENE DeepLTK and CuLab (http://www.ngene.co) and G2CPU (www.g2cpu.com) Toolkits, which enable programmers to directly program PXIe-GPUs from within LabVIEW for Signal, Image and Video Processing, Data Science and ML/DL and LLM Applications under Windows and Linux and, with G2CPU, RT-Linux.

"G2CPU features highly optimized support for GPU-based signal, image, video and data processing, along with advanced multi-GPU scaling, async I/O and GPU-accelerated rendering under Windows, Linux and uniquely, RT-Linux, which is critical for low-latency T&M and Hardware-in-the-Loop projects," said Natan Biesmans, G2CPU CEO. "With these new Catalyst PXIe-GPUs in a PXIe-1095 (Gen 3) Chassis, low-latency LabVIEW / G2CPU applications can now sustain well over 1 GHz of Bandwidth per Channel with 16-bit I/Q data and 1M Point FFTs- with headroom for post-processing. This is a watershed level of performance for PXIe that can eliminate the need for GPU Servers in most cases."

New RADX Catalyst-X PXIe-GPUs Details, Pricing and Delivery

Based on RADX' patent-pending Catalyst-X Carrier Card Technology that enables PCIe add-in-cards to be easily adapted to PXIe, these new models complement the existing lineup of RADX Trifecta and Catalyst PXIe-GPUs, as well as RADX Orca-C2X™ PCIe-to-PXIe eGPUs. These new passively cooled, single-slot, PCIe Gen 4 x8 models, excluding the Ada L4 DC model, include 4 x miniDP outputs with support for up to 8k resolution. Pricing on these new models, all of which feature a US COO and BAA/TAA Compliance ranges from $5,999 to $15,999 USD, excluding US import tariffs on GPUs, with < 60 day ARO delivery beginning in late Q225. For more info, visit www.radxtech.com/pxie-gpus.

RADX NEW Catalyst-X PXIe-GPU Models (P/Ns)	Integrated NVIDIA Pro RTX GPU	PXIe Slots / Watts	GDDR6 / GB/Sec	CUDA, RT & Tensor Cores	FP32 TFLOPS	DP Out	NVENC / NVDEC	COO / ECCN	Q225 MSRP (Excluding US GPU Import Tariffs)
PXIe-GPU-Ampere-RTXA1k-8GB-1SP	Ampere RTX A1000 8GB	1 / 50W	8 GB / 192 GB/Sec	2,304 C / 18 RT / 72 T	6.7	4	G7x1 / G5x1	US / 4A994.L	$5,999
PXIe-GPU-Ada-RTX2k-16GB-1SP	Ada RTX 2000 16GB	1 / 72W	16 GB / 240 GB/Sec	2,816 C / 22 RT / 88 T	12.0	4	G8x1 / G5x1	US / 4A994.L	$9,999
PXIe-GPU-Ada-RTX4kSFF-20GB-1SP	Ada RTX 4000 SFF 20GB	1 / 72W	20 GB / 280 GB/Sec	6,144 C / 48 RT / 192 T	19.2	4	G8x2 / G5x2	US / 4A994.L	$11,999
PXIe-GPU-Ada-L4-DC-24GB-1SP	Ada L4 Data Center 24GB	1 / 72W	24 GB / 300 GB/Sec	7,424 C / 60 RT / 240 T	30.3	N/A	G8x2 / G5x4	US / 4A090.A	$15,999

Demos at NI Connect 2025

RADX will be demonstrating Trifecta and Catalyst PXIe-GPU Modules, as well as several of the Company's high-performance, high-capacity fixed, removable and hot-swap PXIe Data Storage Solutions at NI Connect 2025, held 28-29APR2025 at the Fort Worth Convention Center (Texas). Stop by for a demo and say, "howdy."

About RADX

Founded in 2011, RADX Technologies, Inc., is a high-tech small business that develops COTS, HPC PXIe and PCIe hardware and software products including PXIe and PCIe GPUs, eGPUs, SSD / RAID Subsystems and Network Interface Cards that enable advanced signal/image/video/data processing and ML/DL/LLM training and inference TM applications within the EW, Semiconductor and Automotive markets. As an Emerson / NI Alliance Silver Partner, RADX focuses on products and solutions that complement the Emerson / NI PXIe and USRP product lines. RADX products are BAA / TAA compliant and are available on GSA from TestMart at https://tinyurl.com/muk72crx. RADX is headquartered in California with development locations in Utah, New Mexico, and Taiwan. For more info on RADX, please visit www.radxtech.com, email info@radxtech.com or call +1 (619) 677-1849 x1.

###